AMENDMENTS TO THE SPECIFICATION:

At page 8, please replace the third full paragraph with the following new paragraph:

B1

There is further provided a correlator including a first sub-correlator which receives a fixed pattern having a code length N (N = M x K), as an input signal comprised of signals obtained by spreading a fixed word having a length of K symbols ~~symbol~~ (K is a predetermined positive integer), at a rate of M chips/symbol (M is a predetermined positive integer), and detects a correlation value between a k-th (0 ~~e~~ < k < K) symbol having a M chip length, among the fixed pattern, and pseudorandom noise code Sm (m is an integer defined as k x M ≦ m < (k + 1) x M), and a second sub-correlator which receives data corresponding to K symbols, about a correlation value output from the first sub-correlator, and outputs a correlation value between the data and the fixed word.

At pages 8-9, please replace the bridging paragraph with the following new paragraph:

B2 cont

There is further provided a correlator including a first sub-correlator which receives a fixed pattern having a code length N (N = M x K), as an input signal comprised of signals obtained by spreading a fixed word having a length of K symbols ~~symbol~~ (K is a predetermined positive integer), at a rate of M chips/symbol (M is a predetermined positive integer), and detects a correlation value between a k-th (0 ~~e~~ ≦ k < K) symbol having a M chip length, among the fixed pattern, and pseudorandom noise code Sm (m is an integer defined as k x M ≦ m < (k + 1) x M), a memory which stores a predetermined number of correlation values per a symbol which correlation values are transmitted from the first sub-correlator and are different in a phase from one another with respect to the input signal, and which stores correlation values totally corresponding to K symbols ~~symbol~~, and a second sub-correlator which receives data corresponding to K symbols, read out of the memory every ~~the~~ predetermined number,

B2 concl

and outputs a correlation value between the data and the fixed word.

At page 9, please replace the first full paragraph with the following new paragraph:

B3

There is further provided a correlator which receives a fixed pattern having a code length N (N = M x K) which fixed pattern is obtained by spreading a fixed word having a length of K symbols ~~symbol~~ (K is a predetermined positive integer), at a rate of M chips/symbol (M is a predetermined positive integer), including a first sub-correlator which receives the fixed pattern as an input signal, and detects a correlation value between a k-th (0 ~~o~~ ≤ k < K) symbol having a M chip length, among the fixed pattern, and pseudorandom noise code Sm (m is an integer defined as k x M ≤ m < (k + 1) x M), a memory which stores a predetermined number (L) of correlation values per a symbol which correlation values are transmitted from the first sub-correlator and are different in a phase from one another with respect to the input signal, and which stores L x K correlation values totally corresponding to K symbols ~~symbol~~, a reading-address controller which outputs a reading-address used for reading data corresponding to K symbols ~~symbol~~ out of the memory by every L correlation values, and a second sub-correlator which receives the data corresponding to K symbols ~~symbol~~, read out of the memory by every L correlation values, and outputs a correlation value between the data and the fixed word.

At pages 12-13, please replace the bridging paragraph with the following new paragraph:

B4 cont

The first sub-correlator 10 receives an input signal 11 and coefficient series Si (i = 1, 2, - - -, M) having a length M, used for detecting correlation with the input signal 11, detects correlation (multiplication and addition) between them, and outputs the correlation value 12. The second sub-correlator 20 receives the correlation value 12 transmitted from the first sub-correlator 10, and coefficient series Ui (i =1, 2, - - -,

B4 cont'd

K) used for detecting correlation with K output series of the correlation value 12, detects correlation between them, and outputs a correlation value 21.

At page 13, please replace the fourth full paragraph with the following new paragraph:

B5

For instance, if the correlator illustrated in FIG. 8 were comprised of the first sub-correlator 10 and the second sub-correlator 20 both illustrated in FIG. 1(a), a time necessary for calculation of a correlation value is in proportion to not (M x K) ~~(M + K)~~, but (M + K).

At page 14, please replace the first full paragraph with the following new paragraph:

B6

The first sub-correlator 10 receives an input signal 11 and coefficient series Si (i = 1, 2, - - -, M) having a length M, used for detecting correlation with the input signal 11, detects correlation (multiplication and addition) between them, and outputs the correlation value 12. The second sub-correlator 20 receives the correlation value 12 transmitted from the first sub-correlator 10, and coefficient series Ui (i = 1, 2, - - -, K) used for detecting correlation with K output series of the correlation value 12, detects correlation between them, and outputs a correlation value 22. The third sub-correlator 30 receives the correlation value 22 transmitted from the second sub-correlator 20, and coefficient series Vi (i = 1, 2, - - -, L) used for detecting correlation with L output series of the correlation value 22, detects correlation between them, and outputs a correlation value 21.

At page 14, please replace the second full paragraph with the following new paragraph:

B7 cont

In the correlator illustrated in FIG. 1(b) ~~1(c)~~, a total length of the first to third sub-correlators 10, 20 and 30 is equal to (M + K + L). Hence, the correlator can

B1 Cond

significantly reduce a circuit size in comparison with a conventional correlator (length = M x K x L) corresponding to the correlator illustrated in FIG. 1(c). In addition, it is possible to increase an operation rate in calculation of a correlation value.